

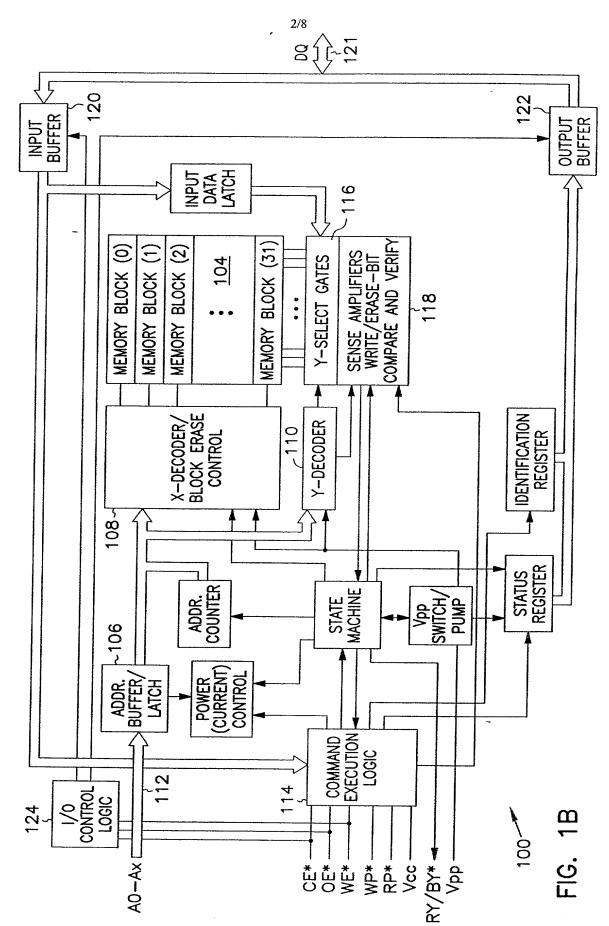
FIG. 1

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TITLE: SUPPLY VOLTAGE REDUCTION CIRCUIT FOR INTEGRATED CIRCUIT

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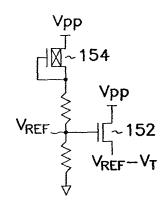
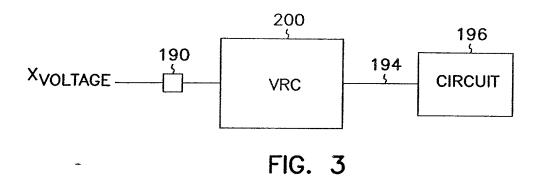


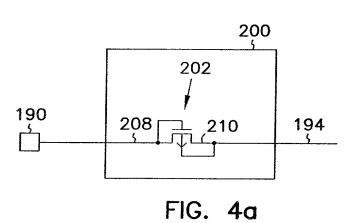
FIG. 2b
$$V_{REF}$$
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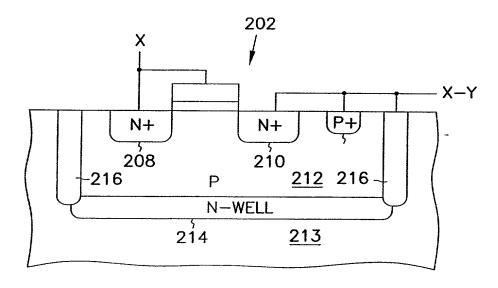


FIG. 4b

TITLE: SUPPLY VOLTAGE REDUCTION CIRCUIT FOR INTEGRATED CIRCUIT INVENTORS NAME: Christophe J. Chevallier DOCKET NO.: 703.019US3

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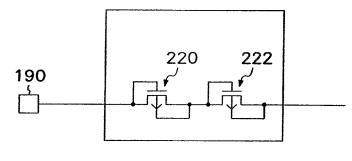


FIG. 5

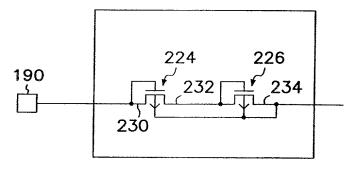


FIG. 6

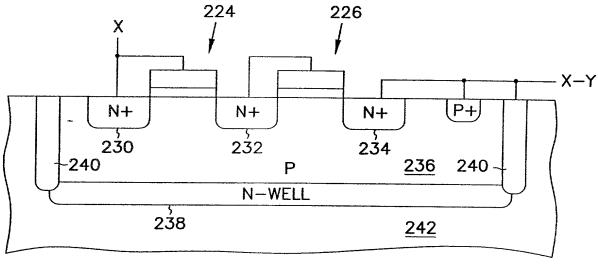
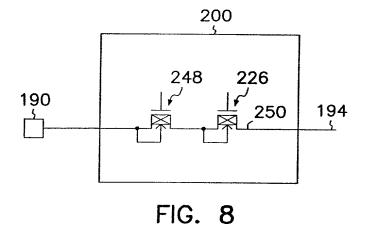
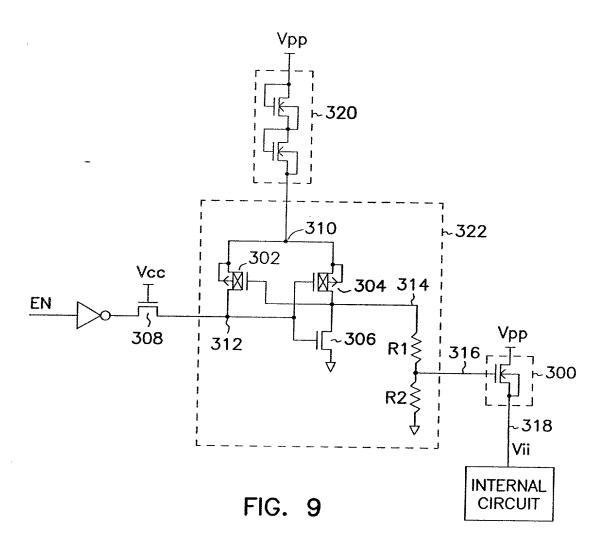


FIG. 7



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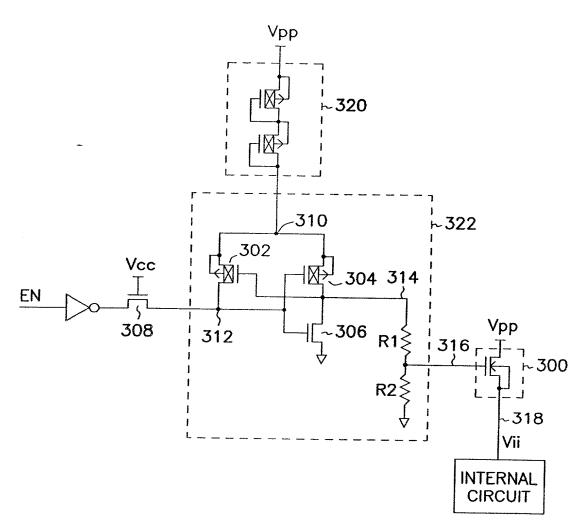


FIG. 10